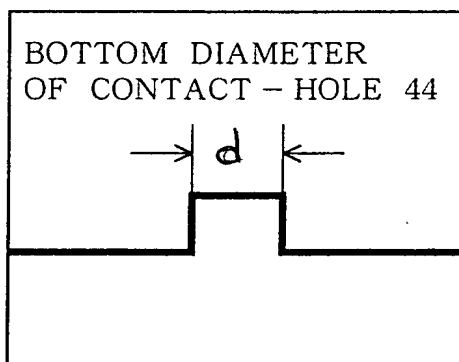


FIG.7a

COMPENSATION CURRENT



POSITION

FIG.7b

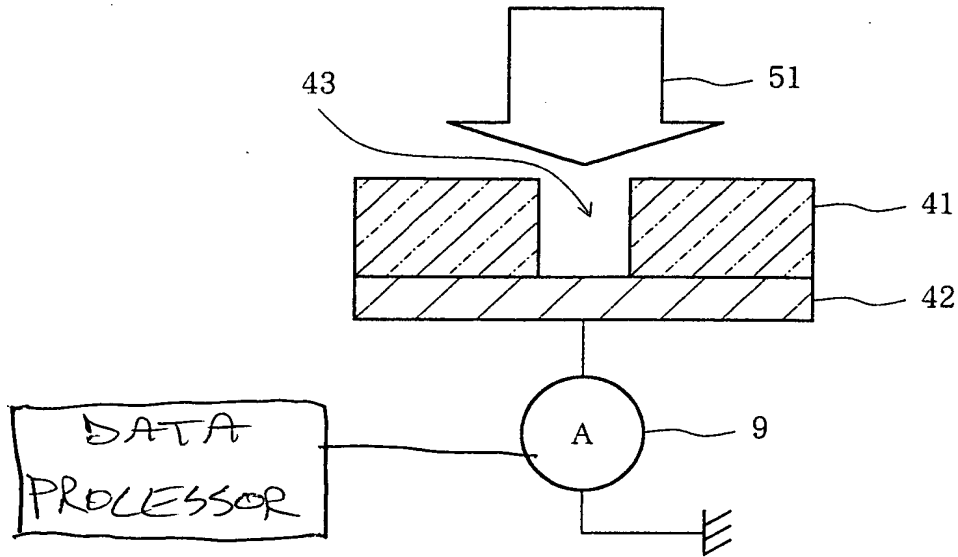
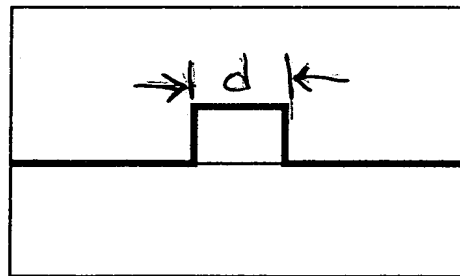


FIG.10a

COMPENSATION CURRENT



POSITION

FIG.10b

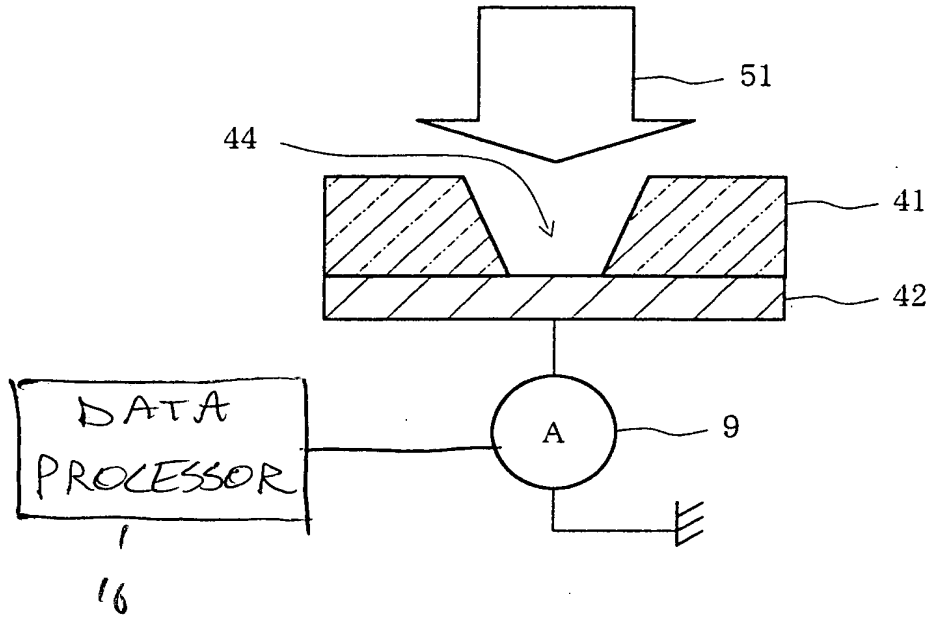
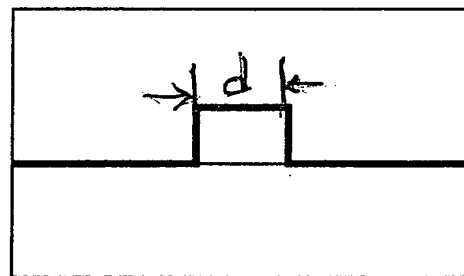


FIG.11a

COMPENSATION CURRENT



POSITION

FIG.11b

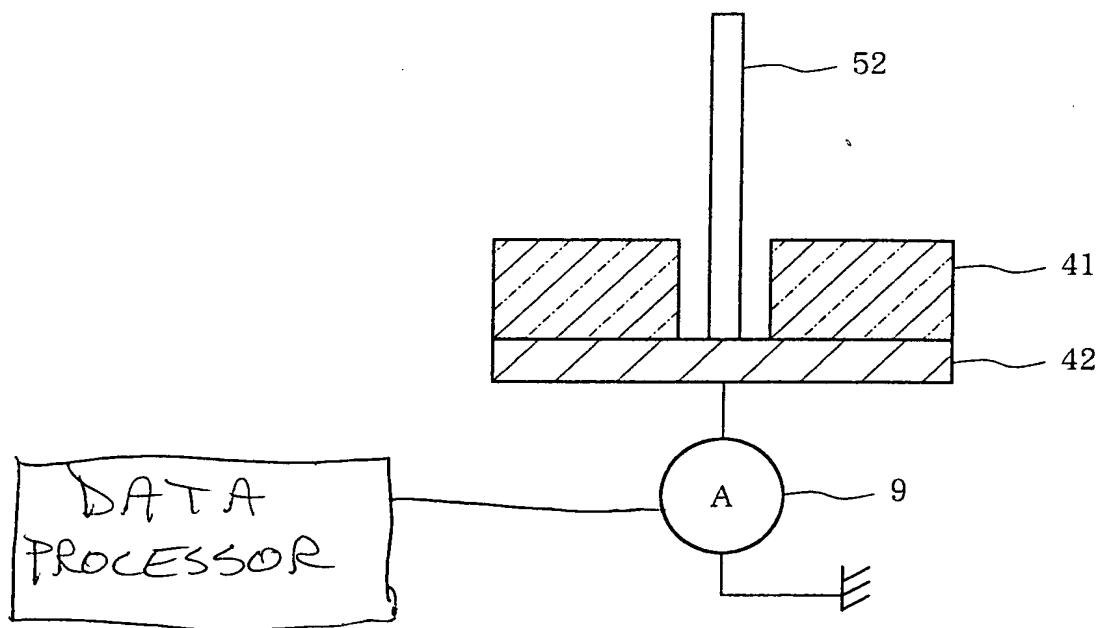
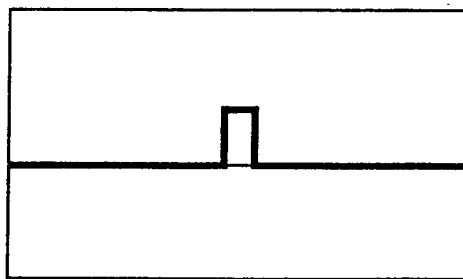


FIG.12a

COMPENSATION CURRENT



POSITION

FIG.12b

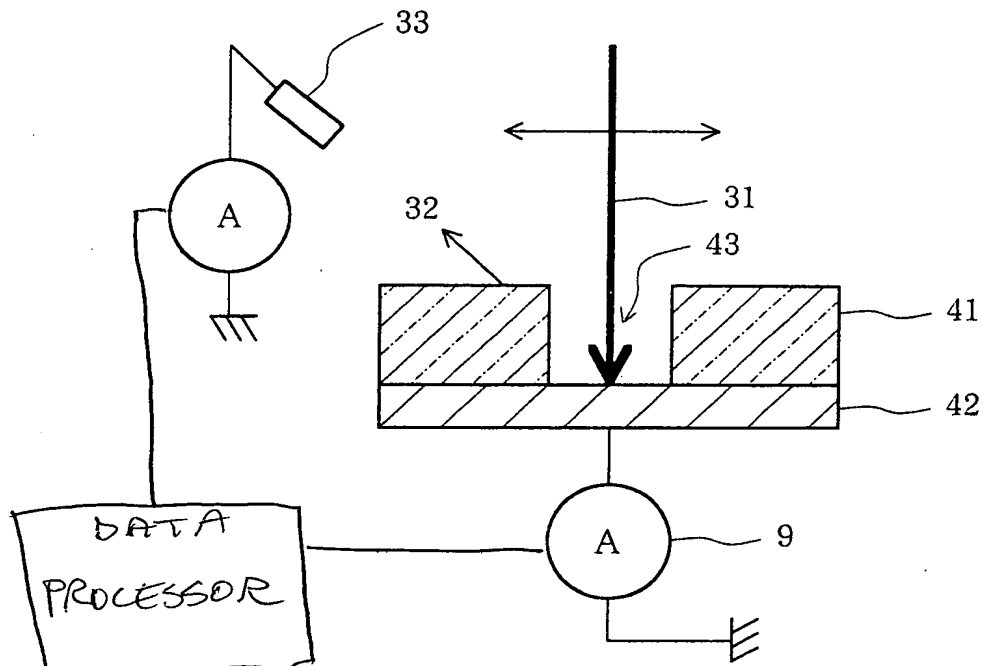
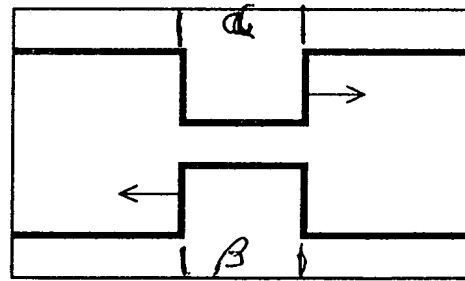


FIG.14a

COMPENSATION CURRENT



POSITION

FIG.14b

AMOUNT OF SECONDARY CURRENT

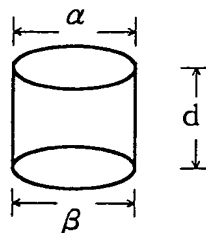


FIG.14c

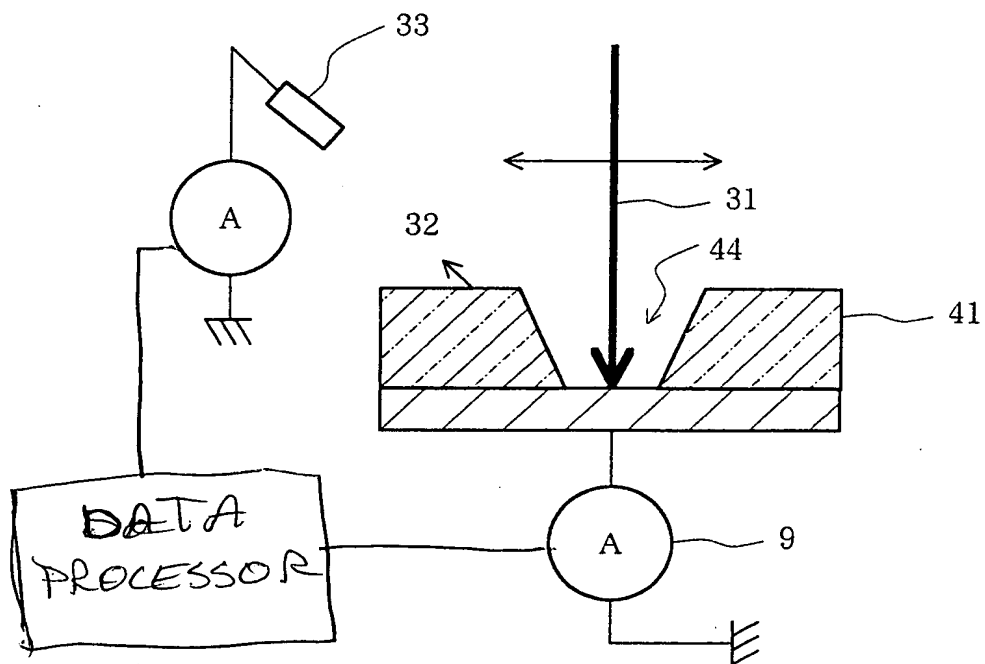
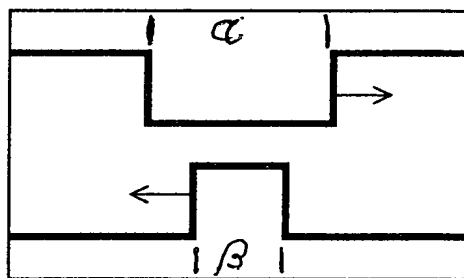


FIG.15a

COMPENSATION CURRENT



POSITION

FIG.15b

AMOUNT OF SECONDARY CURRENT

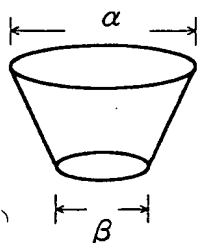


FIG.15c

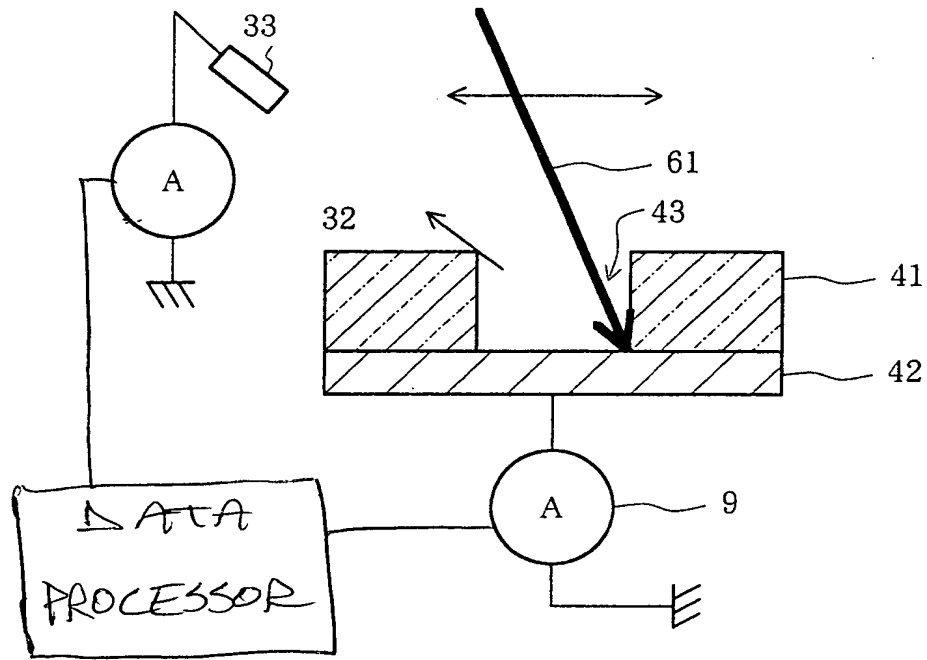


FIG.16a

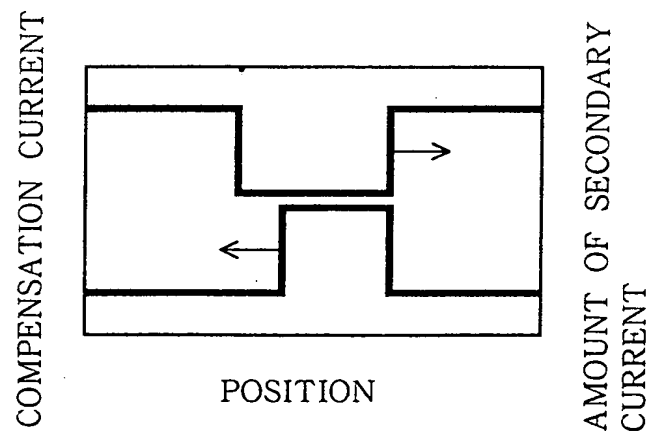
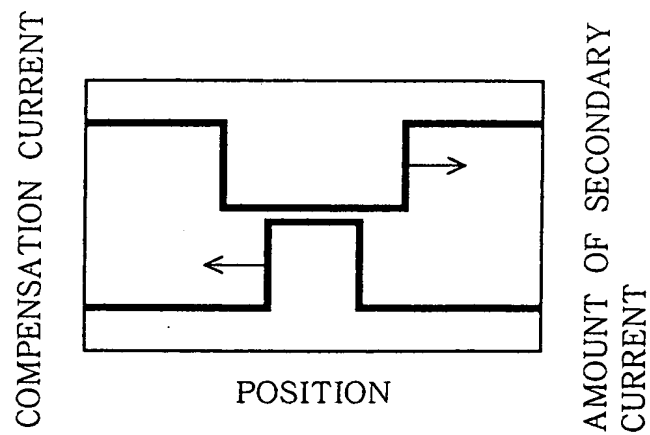
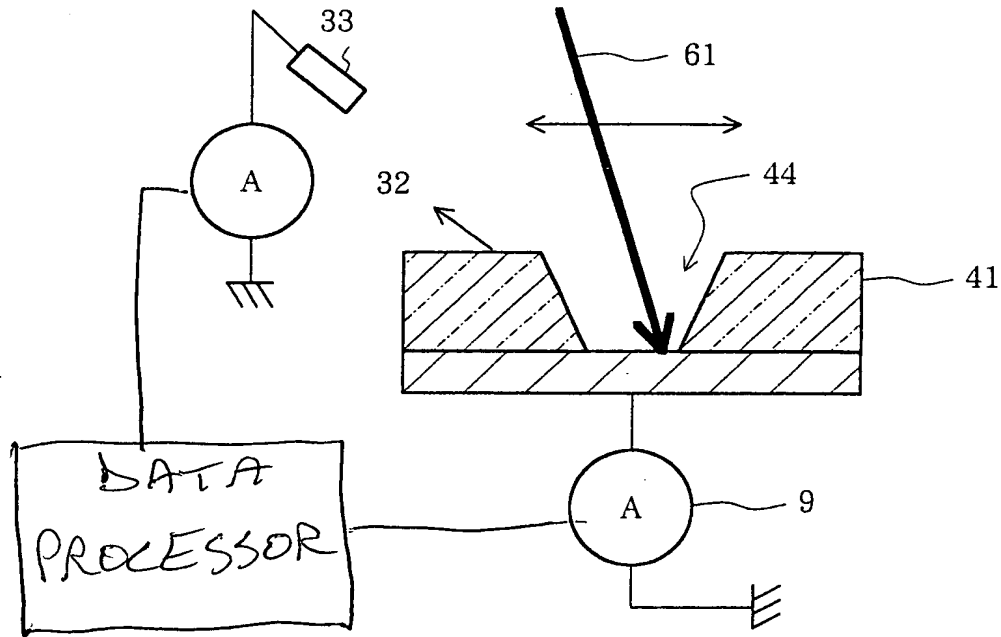


FIG.16b



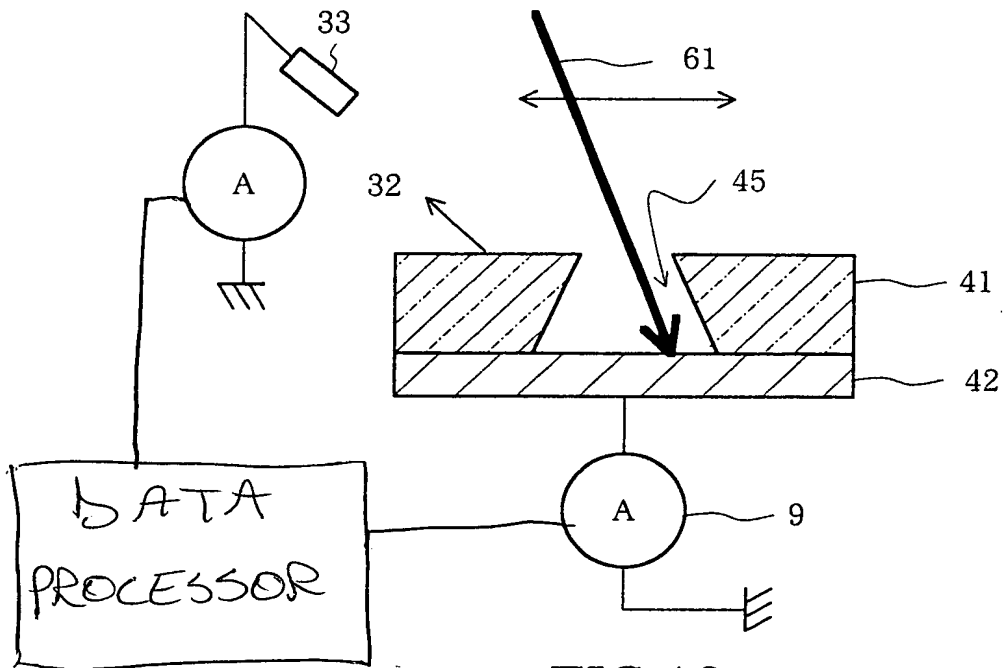


FIG. 18a

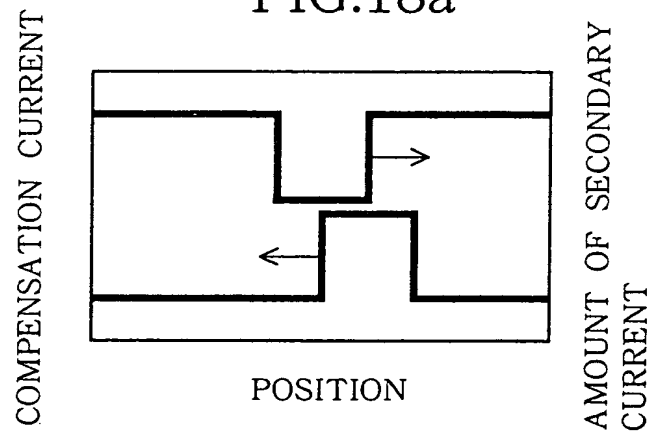


FIG. 18b



FIG. 18c

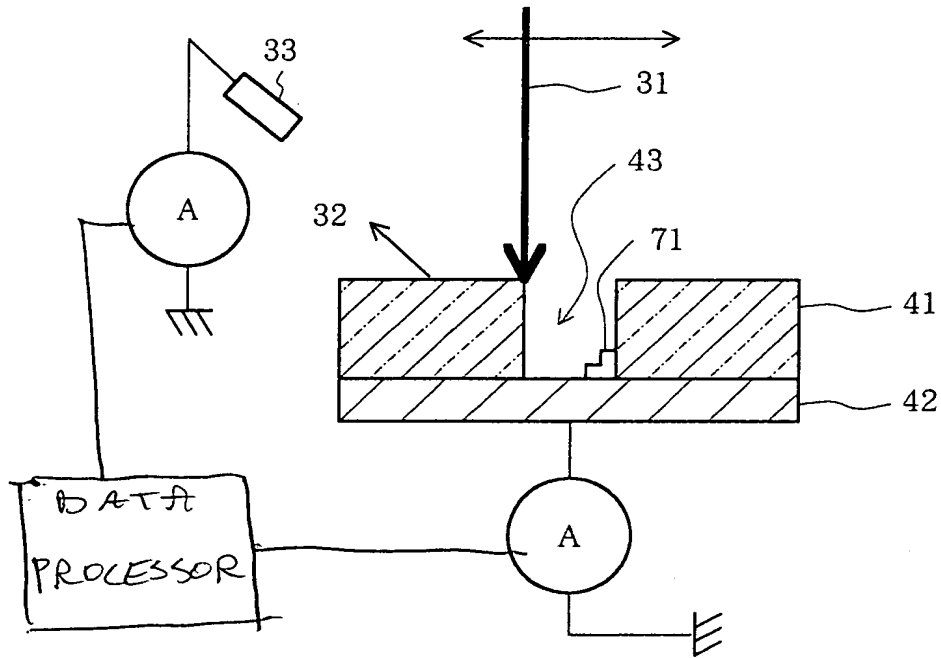


FIG.19a

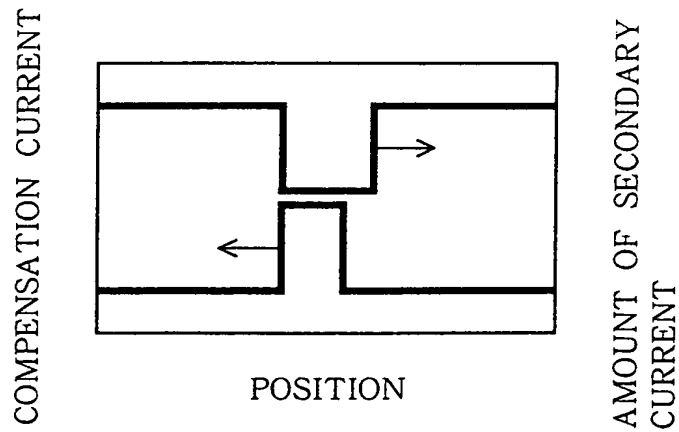


FIG.19b

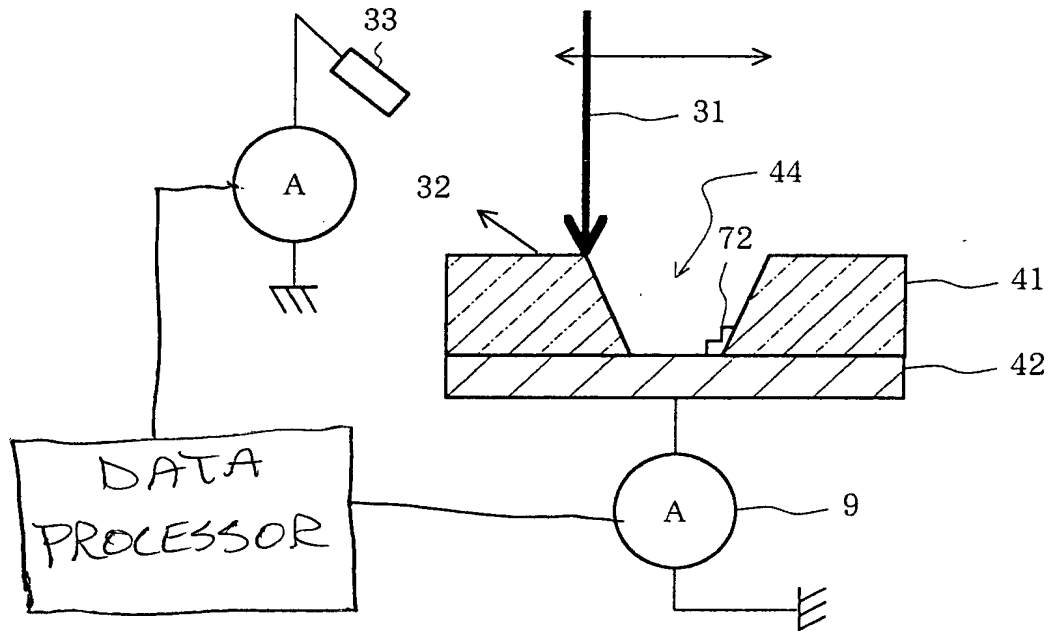


FIG.20a

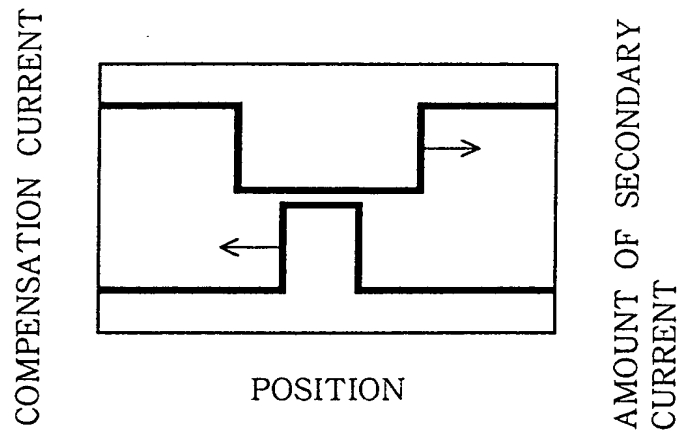


FIG.20b

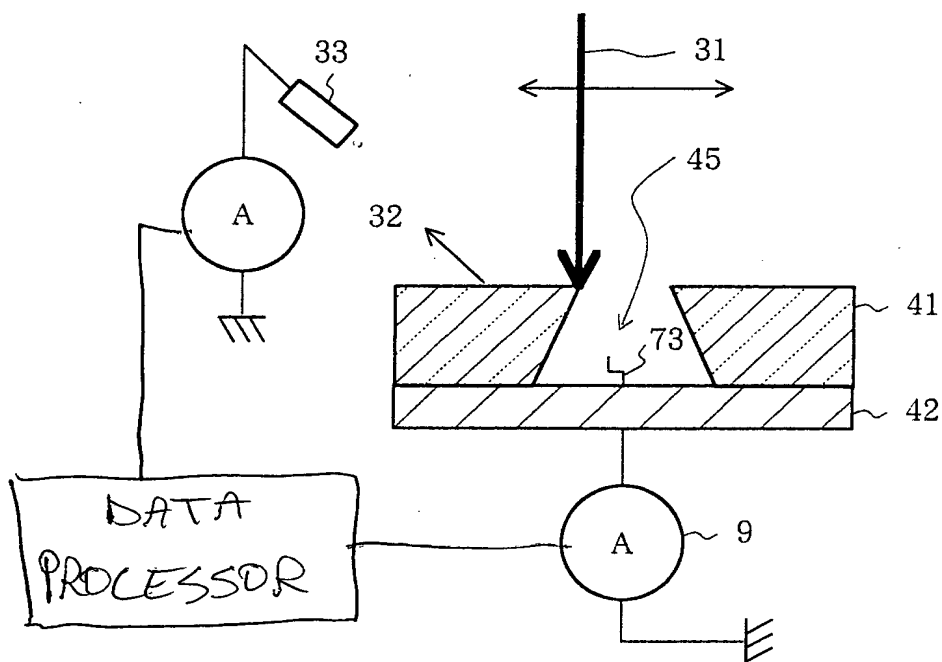


FIG.21a

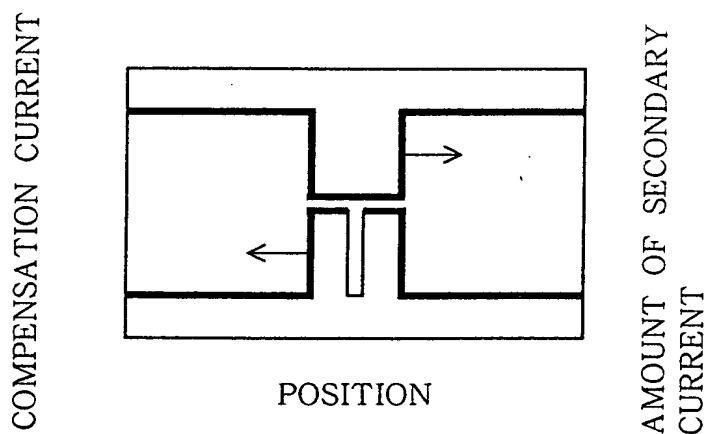


FIG.21b

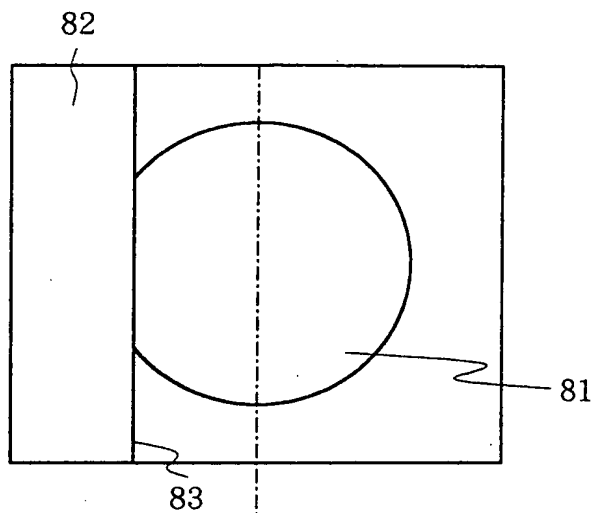


FIG. 22a

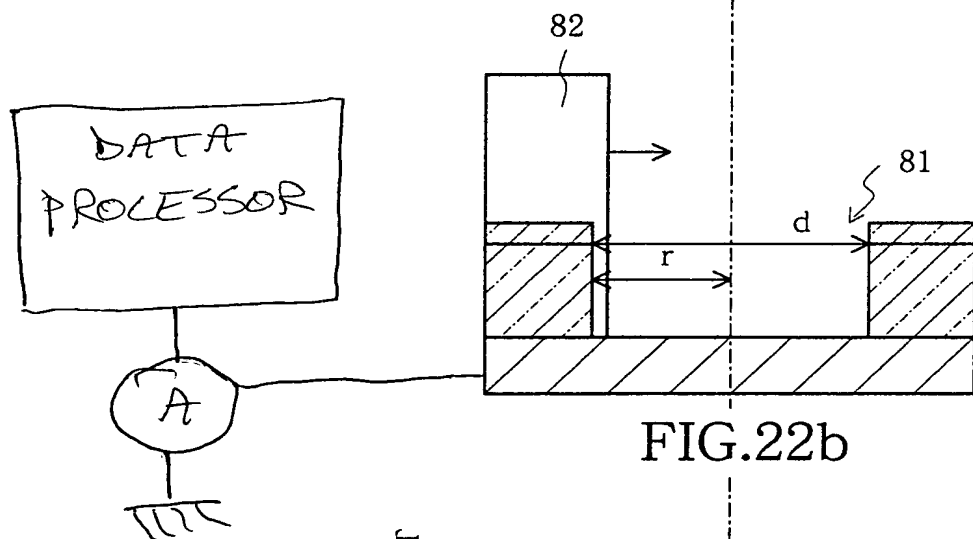


FIG. 22b

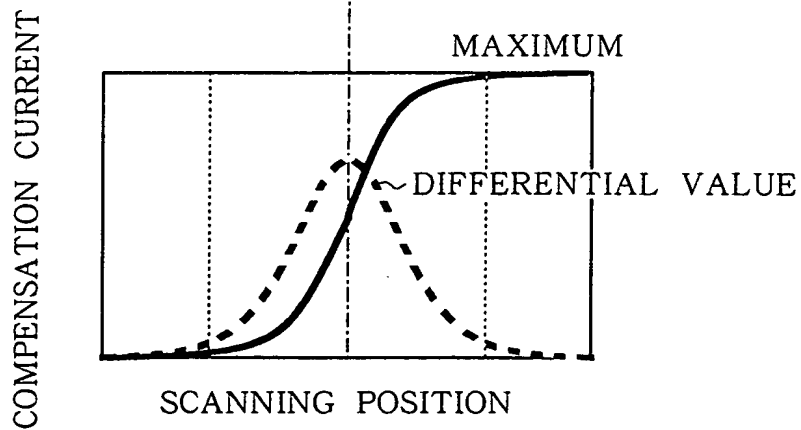
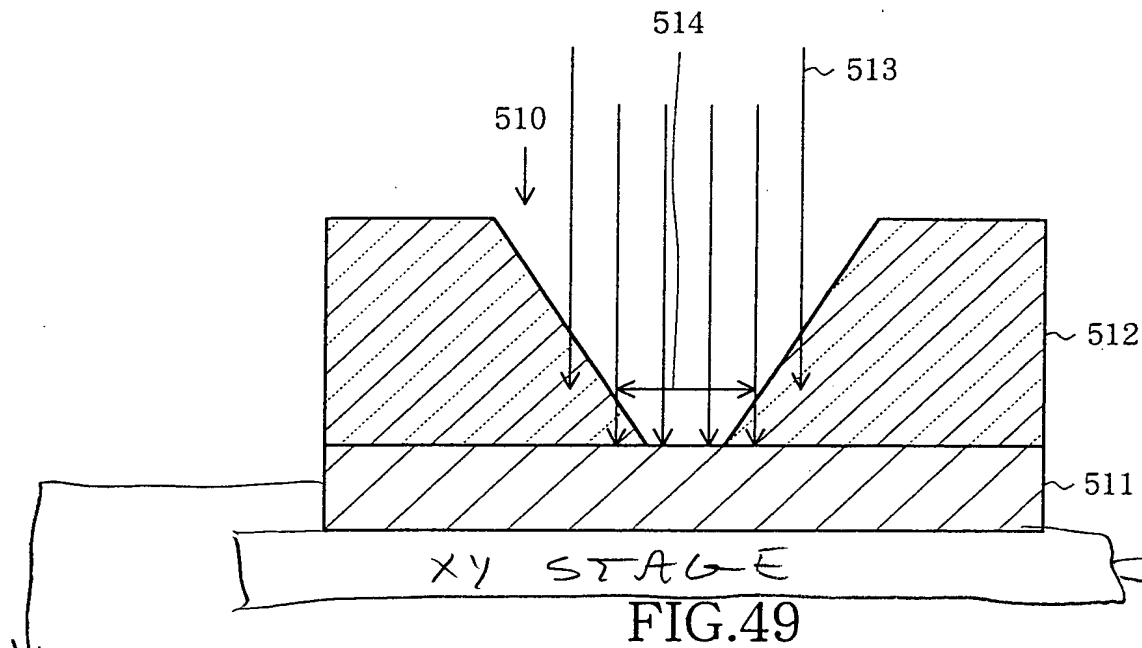


FIG. 22c

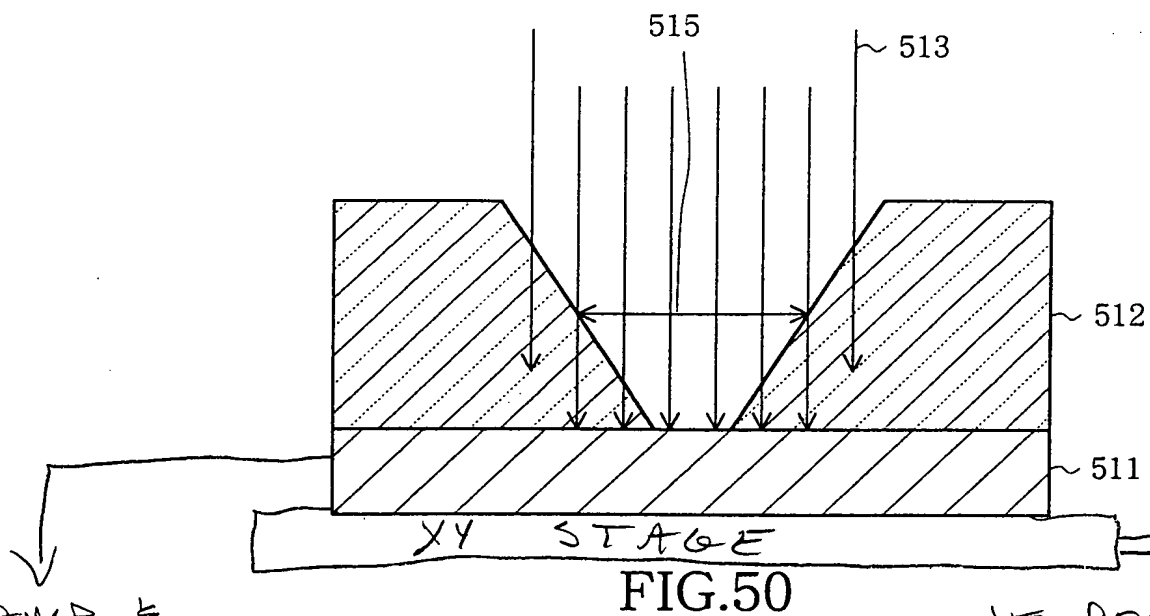


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DATA PROCESSOR

TO POSITION
DETECTOR
& STAGE
CONTROLLER

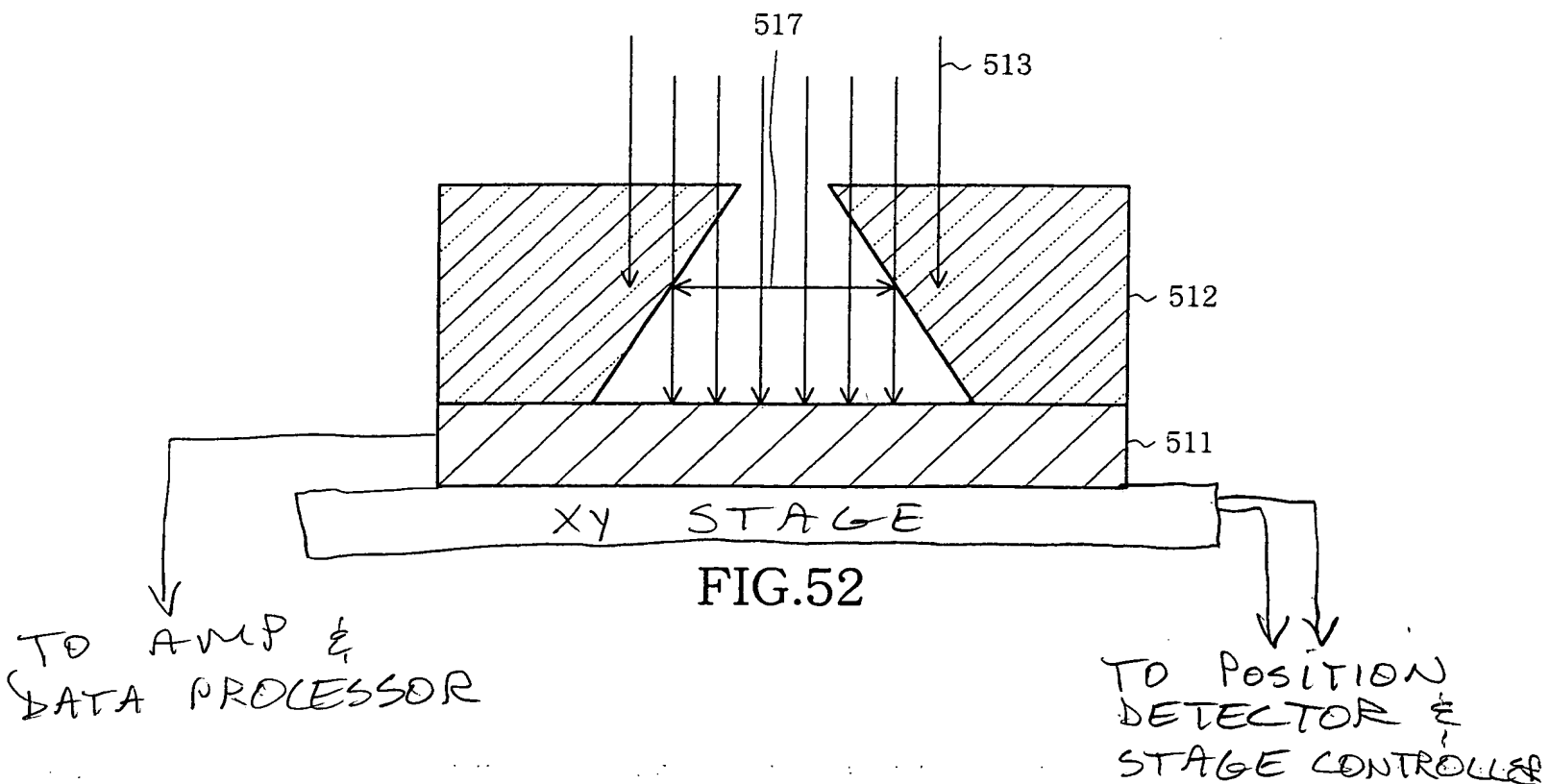
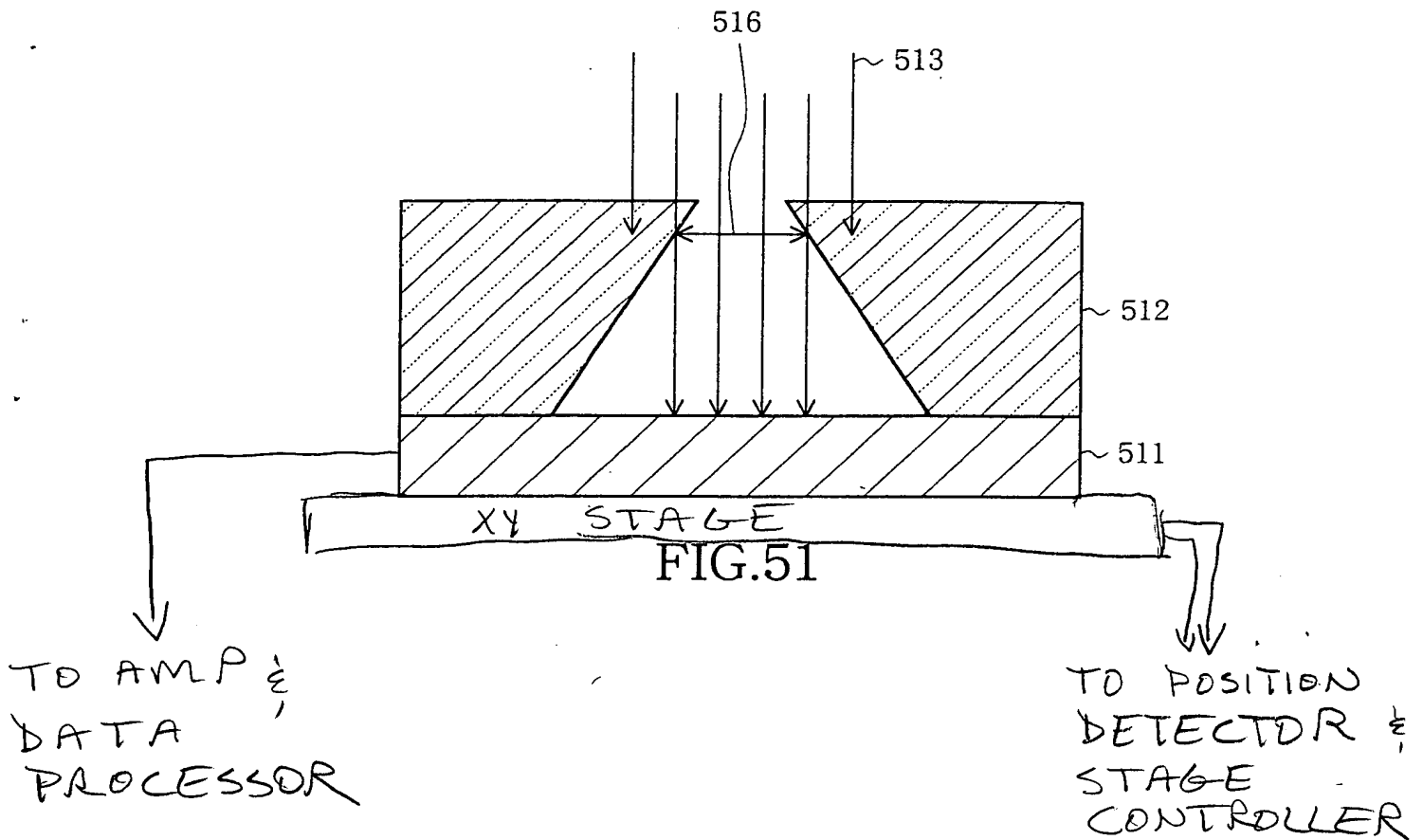


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DATA PROCESSOR

TO POSITION
DETECTOR &
STAGE CONTROLLER

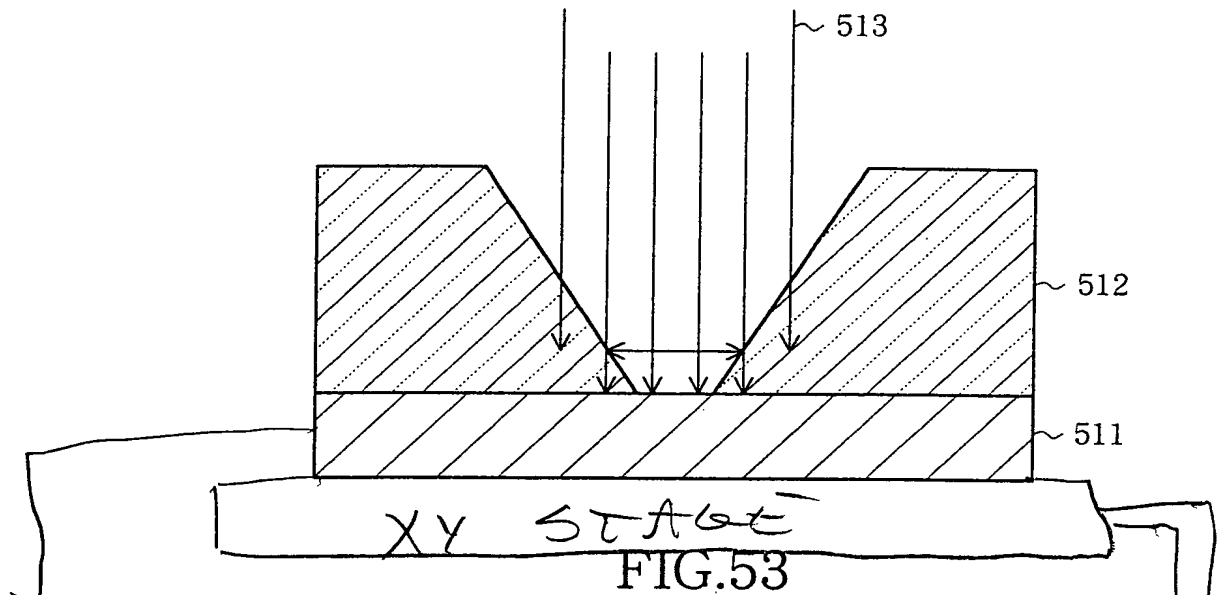


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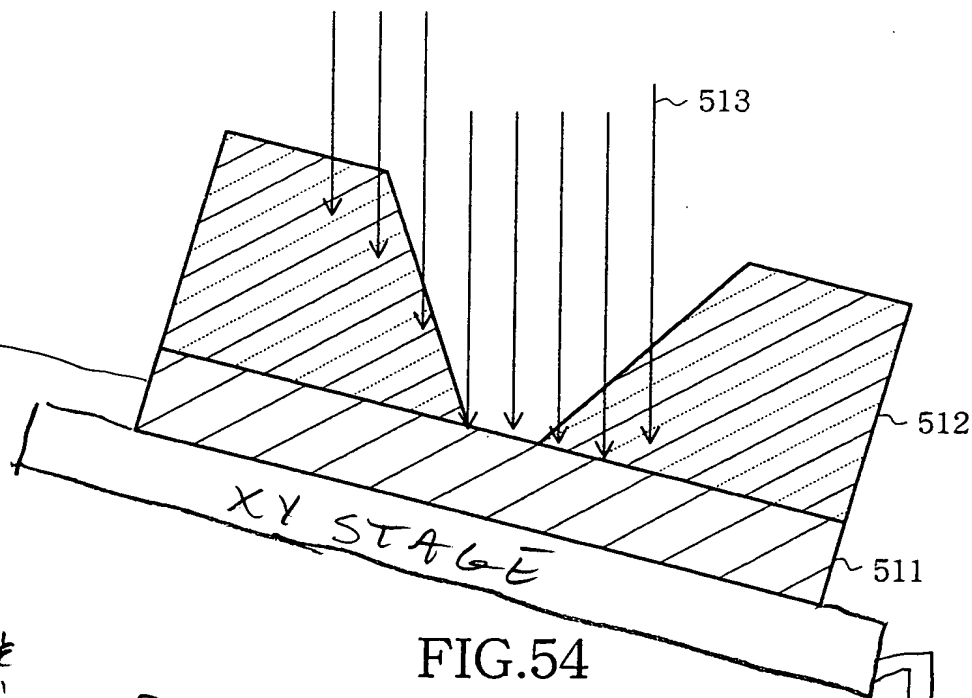


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TO POSITION
DETECTOR &
STAGE CONTROLLER



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